



Howrey Docket No. 01827.0037.CPUS01
Conexant Ref. No. 00CXT0357D CIP

UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Donald B. Eidson, et al.

Application No.: **09/887,877**

Filed: **June 21, 2001**

For: **RATE N/N SYSTEMATIC, RECURSIVE
CONVOLUTIONAL ENCODER AND
CORRESPONDING DECODER**

Art Unit: 2631

Examiner: Unassigned

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**TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.56, 1.97 and 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit the following documents for appropriate action by the U.S. Patent and
Trademark Office:

- Supplemental Information Disclosure Statement under 37 C.F.R. §1.56, 1.97 and 1.98;
- Form PTO-1449 (1 page) with 3 references;
- Return postcard.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

January 30, 2004
Date of Deposit

Robin L. Clow
Name of Person Mailing Paper

R. Clow
Signature of Person Mailing Paper

Applicant believes no fees are due in connection with this submittal. However, if any fees are in fact due, the Commissioner is hereby authorized to be charged to our Deposit Account No. **08-3038** referencing Docket No. 01827.0037.CPUS01.

Respectfully submitted,

Dated: January 30, 2004.

Sean D. Burdick
Sean D. Burdick, Reg. No. 51,513

HOWREY SIMON ARNOLD & WHITE, LLP
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Facsimile: (650) 463-8400
Telephone: (949) 759-5219



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**SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits, without admission of prior art effect thereof, the documents listed on the accompanying Form PTO-1449 pursuant to the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Pursuant to 37 C.F.R. §1.98(d), copies of these documents are not provided herewith given they are being concurrently submitted or otherwise made of record in connection with the parent case, U.S. Patent Application Serial No. 09/602,690, filed June 23, 2000.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached hereto) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits, therefore no statement under 37 C.F.R. § 1.97(e) or fee is required.

It is respectfully requested that the Examiner initial and return a copy of the enclosed Form PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038, referencing Howrey Docket No. 01827.0037.CPUS01.

Respectfully submitted,

Date: January 30, 2004

Sean D. Burdick
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ATTY. DOCKET NO.
01827.0037.CPUS01SERIAL NO.
09/887,877

APPLICANT:

Donald B. Eidson, et al.

FILING DATE:

June 21, 2001

GROUP:

2631

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
				None			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
				None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	A1	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals, Jet Propulsion Laboratory, October 25-29, 1999.
	A2	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART I, Jet Propulsion Laboratory, October 25-29, 1999.
	A3	Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART II, Jet Propulsion Laboratory, October 25-29, 1999.

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EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant